EE: ELECTRICAL ENGINEERING

Duration: Three Hours *Maximum Marks*: 150

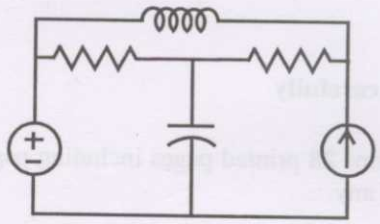
Read the following instructions carefully

1. This question paper contains **28** printed pages including pages for rough work. Please check all pages and report discrepancy, if any.
2. Write your registration number, your name and name of the examination centre at the specified locations on the right half of the ORS.
3. Using HB pencil, darken the appropriate bubble under each digit of your registration number and the letters corresponding to your paper code.
4. All the questions in this question paper are of objective type.
5. Questions must be answered on **O**bjective **R**esponse **S**heet (**ORS**) by darkening the appropriate bubble (marked A, B, C, D) using HB pencil against the question number on the left hand side of the ORS. **Each question has only one correct answer**. In case you wish to change an answer, erase the old answer completely. More than one answer bubbled against a question will be treated as a wrong answer.
6. Questions 1 through 20 are 1-mark questions and questions 21 through 85 are 2-mark questions.
7. Questions 71 through 73 is one set of common data questions, questions 74 and 75 is another pair of common data questions. The question pairs (76, 77), (78, 79), (80, 81), (82, 83) and (84, 85) are questions with linked answers. The answer to the second question of the above pairs will depend on the answer to the first question of the pair. If the first question in the linked pair is wrongly answered or is un-attempted, then the answer to the second question in the pair will not be evaluated.
8. Un-attempted questions will carry zero marks.
9. **NEGATIVE MARKING**: For Q.1 to Q.20, ***0.25*** mark will be deducted for each wrong answer. For Q.21 to Q.75, ***0.5*** mark will be deducted for each wrong answer. For the pairs of questions with linked answers, there will be negative marks only for wrong answer to the first question, i.e. for Q.76, Q.78, Q.80, Q.82 and Q.84, ***0.5*** mark will be deducted for each wrong answer. There is no negative marking for Q.77, Q.79, Q.81, Q.83 and Q.85.
10. Calculator **without data connectivity** is allowed in the examination hall.
11. Charts, graph sheets and tables are NOT allowed in the examination hall.
12. Rough work can be done on the question paper itself. Additional blank pages are given at the end of the question paper for rough work.

Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel
1/24

Q. 1 – Q. 20 carry one mark each.

Q.1 The number of chords in the graph of the given circuit will be



(A) 3 (B) 4 (C) 5 (D) 6

Q.2 The Thevenin's equivalent of a circuit operating at $\omega = 5$ rad/s, has $V_{oc} = 3.71\angle -15.9^\circ$ V and $Z_o = 2.38 - j0.667\ \Omega$. At this frequency, the minimal realization of the Thevenin's impedance will have a

(A) resistor and a capacitor and an inductor
(B) resistor and a capacitor
(C) resistor and an inductor
(D) capacitor and an inductor

Q.3 A signal $e^{-\alpha t}\sin(\omega t)$ is the input to a real Linear Time Invariant system. Given K and ϕ are constants, the output of the system will be of the form $Ke^{-\beta t}\sin(\upsilon t + \phi)$ where

(A) β need not be equal to α but υ equal to ω
(B) υ need not be equal to ω but β equal to α
(C) β equal to α and υ equal to ω
(D) β need not be equal to α and υ need not be equal to ω

Q.4 X is a uniformly distributed random variable that takes values between 0 and 1. The value of $E\{X^3\}$ will be

(A) 0 (B) 1/8 (C) 1/4 (D) 1/2

Q.5 The characteristic equation of a (3×3) matrix $\mathbf{P}$ is defined as

$$\alpha(\lambda) = |\lambda\mathbf{I} - \mathbf{P}| = \lambda^3 + \lambda^2 + 2\lambda + 1 = 0.$$

If $\mathbf{I}$ denotes identity matrix, then the inverse of matrix $\mathbf{P}$ will be

(A) $(\mathbf{P}^2 + \mathbf{P} + 2\mathbf{I})$ (B) $(\mathbf{P}^2 + \mathbf{P} + \mathbf{I})$ (C) $-(\mathbf{P}^2 + \mathbf{P} + \mathbf{I})$ (D) $-(\mathbf{P}^2 + \mathbf{P} + 2\mathbf{I})$

Q.6 If the rank of a (5×6) matrix $\mathbf{Q}$ is 4, then which one of the following statements is correct?

(A) $\mathbf{Q}$ will have four linearly independent rows and four linearly independent columns
(B) $\mathbf{Q}$ will have four linearly independent rows and five linearly independent columns
(C) $\mathbf{Q}\mathbf{Q}^T$ will be invertible
(D) $\mathbf{Q}^T\mathbf{Q}$ will be invertible

Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel 2/24

Q.7 A function $y(t)$ satisfies the following differential equation:

$$\frac{dy(t)}{dt} + y(t) = \delta(t)$$

where $\delta(t)$ is the delta function. Assuming zero initial condition, and denoting the unit step function by $u(t)$, $y(t)$ can be of the form

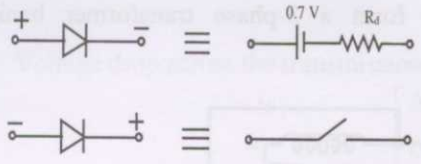
(A) e^{t}

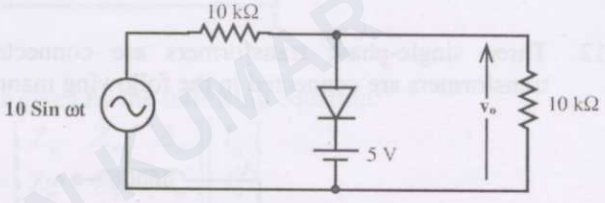
(B) e^{-t}

(C) $e^{t}u(t)$

(D) $e^{-t}u(t)$

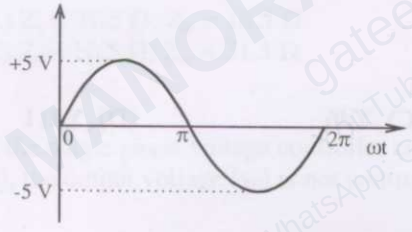
Q.8 The equivalent circuits of a diode, during forward biased and reverse biased conditions, are shown in the figure.



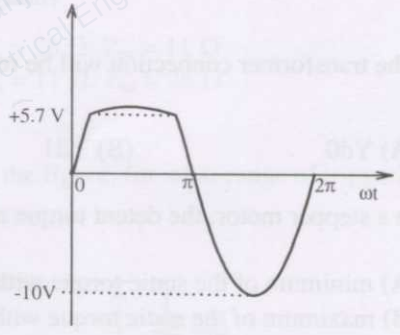


If such a diode is used in clipper circuit of figure given above, the output voltage (v_o) of the circuit will be

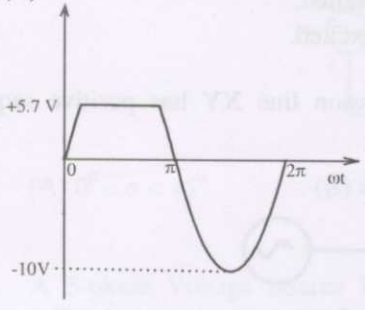
(A)



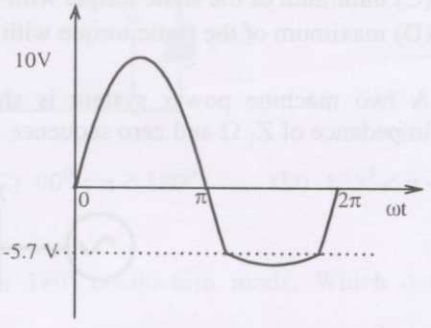
(B)



(C)



(D)



Q.9 Two 8-bit ADCs, one of single slope integrating type and other of successive approximation type, take T_A and T_B times to convert 5 V analog input signal to equivalent digital output. If the input analog signal is reduced to 2.5V, the approximate time taken by the two ADCs will respectively, be

(A) T_A , T_B (B) $T_A/2$, T_B (C) T_A , $T_B/2$ (D) $T_A/2$, $T_B/2$

MANORANJAN KUMAR gateestudy.com YouTube: @Manoranjan_Kr WhatsApp: GATE EE | Electrical Engineering

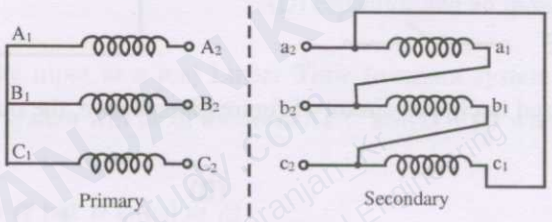
Q.10 An input device is interfaced with Intel 8085A microprocessor as memory mapped I/O. The address of the device is 2500H. In order to input data from the device to accumulator, the sequence of instructions will be

(A) LXI H, 2500H
MOV A, M
(B) LXI H, 2500H
MOV M, A
(C) LHLD 2500H
MOV A, M
(D) LHLD 2500H
MOV M, A

Q.11 Distributed winding and short chording employed in AC machines will result in

(A) increase in emf and reduction in harmonics.
(B) reduction in emf and increase in harmonics.
(C) increase in both emf and harmonics.
(D) reduction in both emf and harmonics.

Q.12 Three single-phase transformers are connected to form a 3-phase transformer bank. The transformers are connected in the following manner:



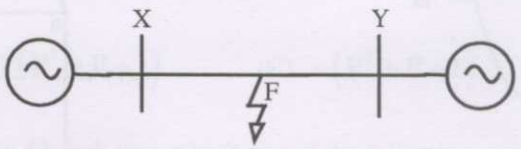
The transformer connection will be represented by

(A) Yd0 (B) Yd1 (C) Yd6 (D) Yd11

Q.13 In a stepper motor, the detent torque means

(A) minimum of the static torque with the phase winding excited.
(B) maximum of the static torque with the phase winding excited.
(C) minimum of the static torque with the phase winding unexcited.
(D) maximum of the static torque with the phase winding unexcited.

Q.14 A two machine power system is shown below. Transmission line XY has positive sequence impedance of $Z_1\ \Omega$ and zero sequence impedance of $Z_0\ \Omega$.



An 'a' phase to ground fault with zero fault impedance occurs at the centre of the transmission line. Bus voltage at X and line current from X to F for the phase 'a', are given by V_a Volts and I_a Amperes, respectively. Then, the impedance measured by the ground distance relay located at the terminal X of line XY will be given by

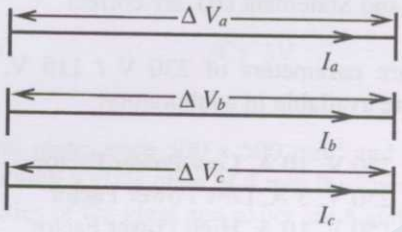
(A) $Z_1/2\ \Omega$ (B) $Z_0/2\ \Omega$ (C) $(Z_0+Z_1)/2\ \Omega$ (D) $V_a/I_a\ \Omega$

Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel
4/24

Q.15 An extra high voltage transmission line of length 300 km can be approximated by a lossless line having propagation constant $\beta = 0.00127$ radians per km. Then the percentage ratio of line length to wavelength will be given by

(A) 24.24 % (B) 12.12 % (C) 19.05 % (D) 6.06 %

Q.16 A 3-phase transmission line is shown in the figure:



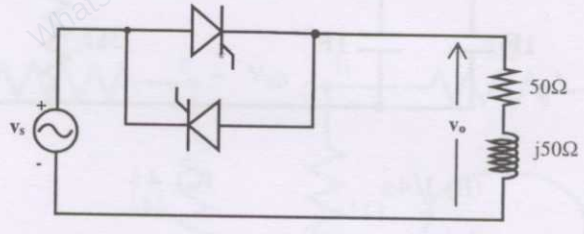
Voltage drop across the transmission line is given by the following equation:

$$\begin{bmatrix} \Delta V_a \\ \Delta V_b \\ \Delta V_c \end{bmatrix} = \begin{bmatrix} Z_s & Z_m & Z_m \\ Z_m & Z_s & Z_m \\ Z_m & Z_m & Z_s \end{bmatrix} \begin{bmatrix} I_a \\ I_b \\ I_c \end{bmatrix}$$

Shunt capacitance of the line can be neglected. If the line has positive sequence impedance of 15 Ω and zero sequence impedance of 48 Ω, then the values of Z_s and Z_m will be

(A) $Z_s = 31.5\ \Omega$; $Z_m = 16.5\ \Omega$
(B) $Z_s = 26\ \Omega$; $Z_m = 11\ \Omega$
(C) $Z_s = 16.5\ \Omega$; $Z_m = 31.5\ \Omega$
(D) $Z_s = 11\ \Omega$; $Z_m = 26\ \Omega$

Q.17 In the single phase voltage controller circuit shown in the figure, for what range of triggering angle (α), the output voltage (v_0) is not controllable?



(A) $0^0 < \alpha < 45^0$ (B) $45^0 < \alpha < 135^0$ (C) $90^0 < \alpha < 180^0$ (D) $135^0 < \alpha < 180^0$

Q.18 A 3-phase Voltage Source Inverter is operated in 180^0 conduction mode. Which one of the following statements is true?

(A) Both pole-voltage and line-voltage will have 3rd harmonic components
(B) Pole-voltage will have 3rd harmonic component but line-voltage will be free from 3rd harmonic
(C) Line-voltage will have 3rd harmonic component but pole-voltage will be free from 3rd harmonic
(D) Both pole-voltage and line-voltage will be free from 3rd harmonic components

Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel
5/24

Q.19 The impulse response of a causal linear time-invariant system is given as h(t). Now consider the following two statements:
Statement (I): Principle of superposition holds
Statement (II): $h(t) = 0$ for $t < 0$.

Which one of the following statements is correct?

(A) Statement (I) is correct and Statement (II) is wrong
(B) Statement (II) is correct and Statement (I) is wrong
(C) Both Statement (I) and Statement (II) are wrong
(D) Both Statement (I) and Statement (II) are correct

Q.20 It is desired to measure parameters of 230 V / 115 V, 2 kVA, single-phase transformer. The following wattmeters are available in a laboratory:

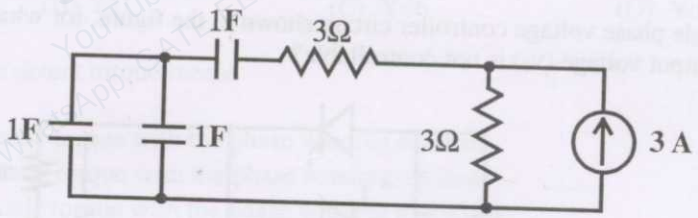
W_1: 250 V, 10 A, Low Power Factor
W_2: 250 V, 5 A, Low Power Factor
W_3: 150 V, 10 A, High Power Factor
W_4: 150 V, 5 A, High Power Factor

The wattmeters used in open circuit test and short circuit test of the transformer will respectively be

(A) W_1 and W_2
(B) W_2 and W_4
(C) W_1 and W_4
(D) W_2 and W_3

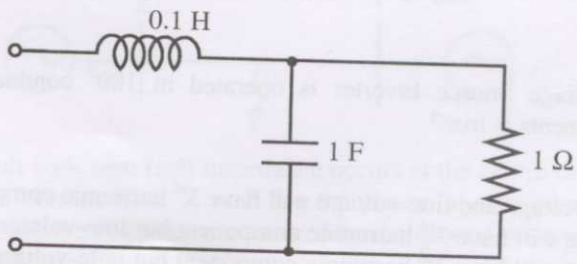
Q. 21 to Q.75 carry two marks each.

Q.21 The time constant for the given circuit will be



(A) 1/9 s (B) 1/4 s (C) 4 s (D) 9 s

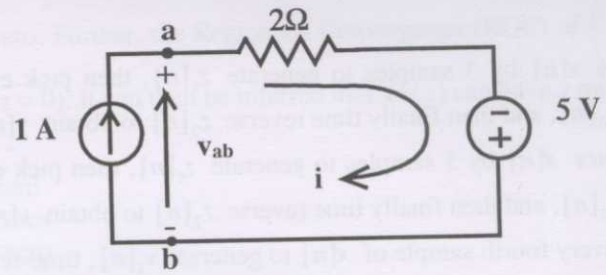
Q.22 The resonant frequency for the given circuit will be



(A) 1 rad/s (B) 2 rad/s (C) 3 rad/s (D) 4 rad/s

Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel
6/24

Q.23 Assuming ideal elements in the circuit shown below, the voltage v_{ab} will be



(A) -3V (B) 0 V (C) 3V (D) 5 V

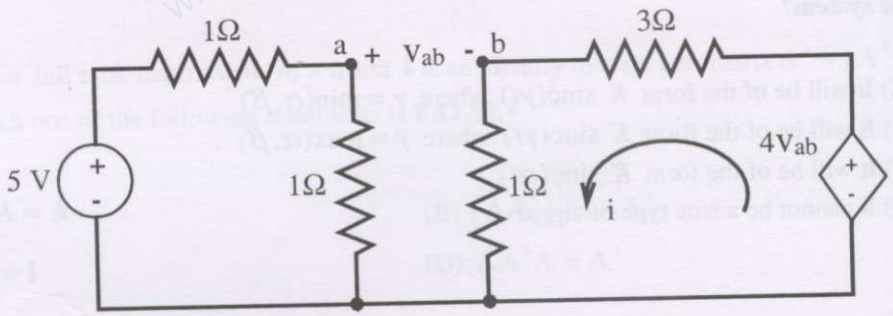
Q.24 A capacitor consists of two metal plates each 500 x 500 mm^2 and spaced 6 mm apart. The space between the metal plates is filled with a glass plate of 4 mm thickness and a layer of paper of 2 mm thickness. The relative permittivities of the glass and paper are 8 and 2 respectively. Neglecting the fringing effect, the capacitance will be
(Given that $\varepsilon_0 = 8.85 \times 10^{-12}$ F/m)

(A) 983.33 pF (B) 1475 pF (C) 6637.5 pF (D) 9956.25 pF

Q.25 A coil of 300 turns is wound on a non-magnetic core having a mean circumference of 300 mm and a cross-sectional area of 300 mm^2. The inductance of the coil corresponding to a magnetizing current of 3A will be
(Given that $\mu_0 = 4\pi \times 10^{-7}$ H/m)

(A) 37.68 μH (B) 113.04 μH (C) 37.68 mH (D) 113.04 mH

Q.26 In the circuit shown in the figure, the value of the current i will be given by



(A) 0.31 A (B) 1.25 A (C) 1.75 A (D) 2.5 A

Q.27 Two point charges $Q_1 = 10\ \mu C$ and $Q_2 = 20\ \mu C$ are placed at coordinates (1, 1, 0) and (-1, -1, 0) respectively. The total electric flux passing through a plane z = 20 will be

(A) 7.5 μC (B) 13.5 μC (C) 15.0 μC (D) 22.5 μC

Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel 7/24

Q.28 Given a sequence $x[n]$, to generate the sequence $y[n]=x[3-4n]$, which one of the following procedures would be correct?

(A) First *delay* $x[n]$ by 3 samples to generate $z_1[n]$, then pick every 4^{th} sample of $z_1[n]$ to generate $z_2[n]$, and then finally time reverse $z_2[n]$ to obtain $y[n]$

(B) First *advance* $x[n]$ by 3 samples to generate $z_1[n]$, then pick every 4^{th} sample of $z_1[n]$ to generate $z_2[n]$, and then finally time reverse $z_2[n]$ to obtain $y[n]$

(C) First pick every fourth sample of $x[n]$ to generate $v_1[n]$, time-reverse $v_1[n]$ to obtain $v_2[n]$, and finally *advance* $v_2[n]$ by 3 samples to obtain $y[n]$

(D) First pick every fourth sample of $x[n]$ to generate $v_1[n]$, time-reverse $v_1[n]$ to obtain $v_2[n]$, and finally *delay* $v_2[n]$ by 3 samples to obtain $y[n]$

Q.29 A system with input $x(t)$ and output $y(t)$ is defined by the input-output relation:

$$y(t)=\int_{-\infty}^{-2t} x(\tau)d\tau$$

The system will be

(A) causal, time-invariant and unstable
(B) causal, time-invariant and stable
(C) non-causal, time-invariant and unstable
(D) non-causal, time-variant and unstable

Q.30 A signal $x(t)=\text{sinc}(\alpha t)$ where α is a real constant $\left(\text{sinc}(x)=\dfrac{\sin(\pi x)}{\pi x}\right)$ is the input to a Linear Time invariant system whose impulse response $h(t)=\text{sinc}(\beta t)$ where β is a real constant. If $\min(\alpha,\beta)$ denotes the minimum of α and β, and similarly $\max(\alpha,\beta)$ denotes the maximum of α and β, and K is a constant, which one of the following statements is true about the **output of the system**?

(A) It will be of the form $K\ \text{sinc}(\gamma t)$ where $\gamma=\min(\alpha,\beta)$
(B) It will be of the form $K\ \text{sinc}(\gamma t)$ where $\gamma=\max(\alpha,\beta)$
(C) It will be of the form $K\ \text{sinc}(\alpha t)$
(D) It cannot be a sinc type of signal

Q.31 Let $x(t)$ be a periodic signal with time period T. Let $y(t)=x(t-t_0)+x(t+t_0)$ for some t_0. The Fourier Series coefficients of $y(t)$ are denoted by b_k. If $b_k=0$ for all odd k, then t_0 can be equal to

(A) $T/8$ (B) $T/4$ (C) $T/2$ (D) $2T$

Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel
8/24

Q.32 $H(z)$ is a transfer function of a *real* system. When a signal $x[n]=(1+j)^n$ is the input to such a system, the output is zero. Further, the Region Of Convergence (ROC) of $\left(1-\frac{1}{2}z^{-1}\right)H(z)$ is the entire Z-plane (except $z=0$). It can then be inferred that $H(z)$ can have a minimum of

(A) one pole and one zero
(B) one pole and two zeros
(C) two poles and one zero
(D) two poles and two zeros

Q.33 Given $X(z)=\frac{z}{(z-a)^2}$ with $|z|>a$, the residue of $X(z)z^{n-1}$ at $z=a$ for $n\geq 0$ will be

(A) a^{n-1} (B) a^n (C) na^n (D) na^{n-1}

Q.34 Consider function $f(x)=(x^2-4)^2$ where x is a real number. Then the function has

(A) only one minimum
(B) only two minima
(C) three minima
(D) three maxima

Q.35 Equation $e^x-1=0$ is required to be solved using Newton's method with an initial guess $x_o=-1$. Then, after one step of Newton's method, estimate x_1 of the solution will be given by

(A) 0.71828 (B) 0.36784 (C) 0.20587 (D) 0.00000

Q.36 $\mathbf{A}$ is a $m\times n$ full rank matrix with $m>n$ and $\mathbf{I}$ is an identity matrix. Let matrix $\mathbf{A}^+=\left(\mathbf{A}^T\mathbf{A}\right)^{-1}\mathbf{A}^T$. Then, which one of the following statements is **FALSE**?

(A) $\mathbf{AA}^+\mathbf{A}=\mathbf{A}$
(B) $\left(\mathbf{AA}^+\right)^2=\mathbf{AA}^+$
(C) $\mathbf{A}^+\mathbf{A}=\mathbf{I}$
(D) $\mathbf{AA}^+\mathbf{A}=\mathbf{A}^+$

Q.37 A differential equation $\frac{dx}{dt}=e^{-2t}u(t)$ has to be solved using trapezoidal rule of integration with a step size h=0.01 s. Function $u(t)$ indicates a unit step function. If $x(0-)=0$, then value of x at t=0.01 s will be given by:

(A) 0.00099 (B) 0.00495 (C) 0.0099 (D) 0.0198

Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel
9/24

Q.38 Let $\mathbf{P}$ be a 2×2 real orthogonal matrix and $\vec{\mathbf{x}}$ is a real vector $[x_1, x_2]^T$ with length $\|\vec{\mathbf{x}}\| = (x_1^2 + x_2^2)^{1/2}$. Then, which one of the following statements is correct?

(A) $\|\mathbf{P}\vec{\mathbf{x}}\| \le \|\vec{\mathbf{x}}\|$ where at least one vector satisfies $\|\mathbf{P}\vec{\mathbf{x}}\| < \|\vec{\mathbf{x}}\|$

(B) $\|\mathbf{P}\vec{\mathbf{x}}\| = \|\vec{\mathbf{x}}\|$ for all vectors $\vec{\mathbf{x}}$

(C) $\|\mathbf{P}\vec{\mathbf{x}}\| \ge \|\vec{\mathbf{x}}\|$ where at least one vector satisfies $\|\mathbf{P}\vec{\mathbf{x}}\| > \|\vec{\mathbf{x}}\|$

(D) No relationship can be established between $\|\vec{\mathbf{x}}\|$ and $\|\mathbf{P}\vec{\mathbf{x}}\|$

Q.39 Let $x(t) = rect(t - \frac{1}{2})$ (where $rect(x) = 1$ for $-\frac{1}{2} \le x \le \frac{1}{2}$ and zero otherwise). Then if $\text{sinc}(x) = \frac{\sin(\pi x)}{\pi x}$, the Fourier Transform of $x(t) + x(-t)$ will be given by

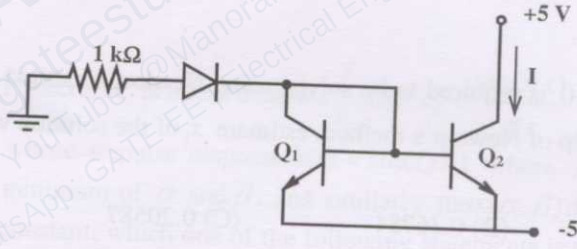
(A) $\text{sinc}\left(\frac{\omega}{2\pi}\right)$

(B) $2\,\text{sinc}\left(\frac{\omega}{2\pi}\right)$

(C) $2\,\text{sinc}\left(\frac{\omega}{2\pi}\right)\cos\left(\frac{\omega}{2}\right)$

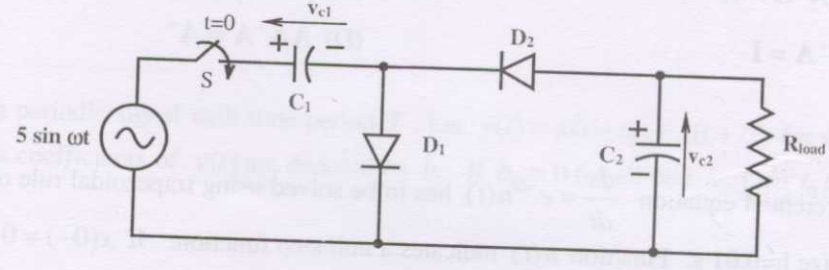
(D) $\text{sinc}\left(\frac{\omega}{2\pi}\right)\sin\left(\frac{\omega}{2}\right)$

Q.40 Two perfectly matched silicon transistors are connected as shown in the figure. Assuming the β of the transistors to be very high and the forward voltage drop in diodes to be 0.7V, the value of current I is



(A) 0 mA (B) 3.6 mA (C) 4.3 mA (D) 5.7 mA

Q.41 In the voltage doubler circuit shown in the figure, the switch 'S' is closed at t = 0. Assuming diodes D_1 and D_2 to be ideal, **load resistance to be infinite and initial capacitor voltages to be zero**, the steady state voltage across capacitors C_1 and C_2 will be



(A) $v_{c1} = 10V, v_{c2} = 5V$

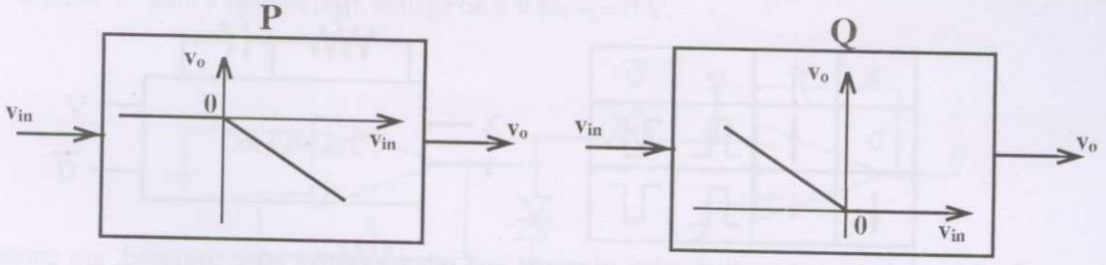
(B) $v_{c1} = 10V, v_{c2} = -5V$

(C) $v_{c1} = 5V, v_{c2} = 10V$

(D) $v_{c1} = 5V, v_{c2} = -10V$

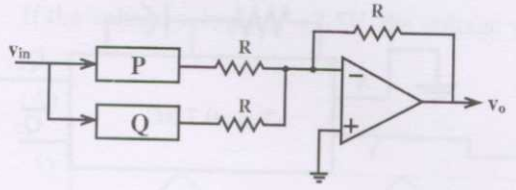
Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel

Q.42 The block diagrams of two types of half wave rectifiers are shown in the figure. The transfer characteristics of the rectifiers are also shown within the block.

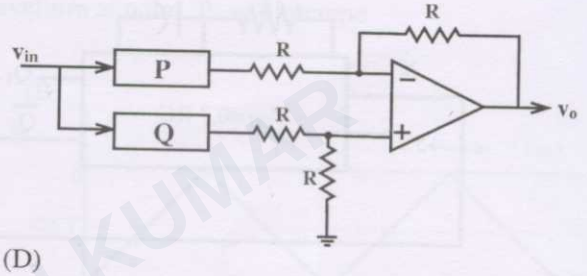


It is desired to make full wave rectifier using above two half-wave rectifiers. The resultant circuit will be

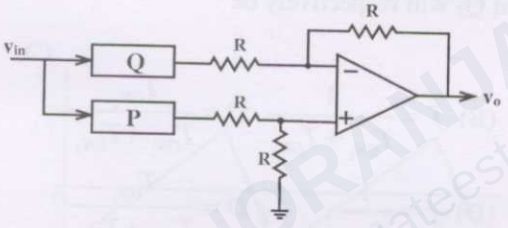
(A)



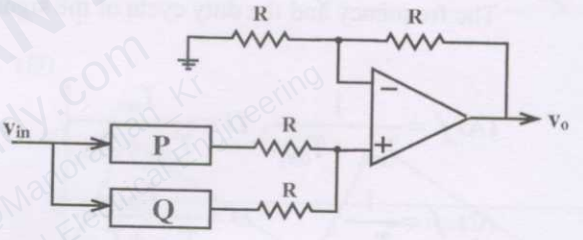
(B)



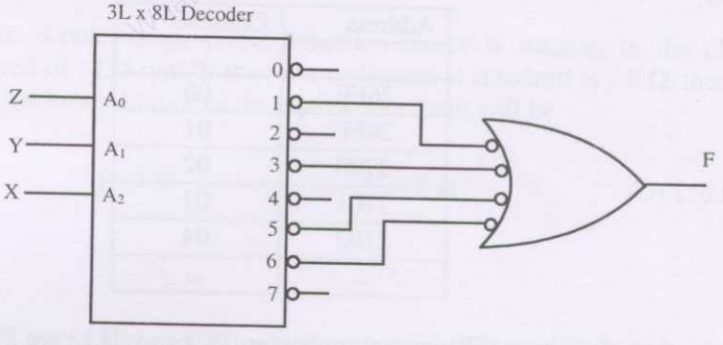
(C)



(D)



Q.43 A 3 line to 8 line decoder, with active low outputs, is used to implement a 3-variable Boolean function as shown in the figure.



The simplified form of Boolean function F(A,B,C) implemented in 'Product of Sum' form will be

(A) $(X+Z).(\overline{X}+\overline{Y}+\overline{Z}).(Y+Z)$

(B) $(\overline{X}+\overline{Z}).(X+Y+Z).(\overline{Y}+\overline{Z})$

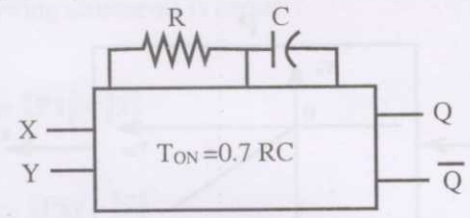
(C) $(\overline{X}+\overline{Y}+Z).(\overline{X}+Y+Z).(X+\overline{Y}+Z).(X+Y+\overline{Z})$

(D) $(\overline{X}+\overline{Y}+\overline{Z}).(\overline{X}+Y+\overline{Z}).(X+Y+Z).(X+\overline{Y}+\overline{Z})$

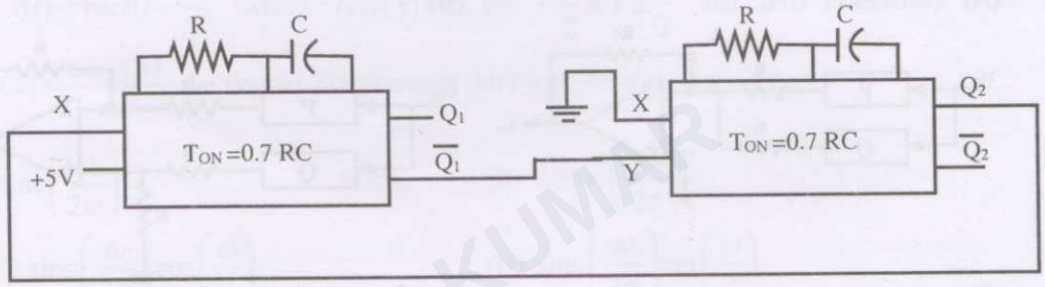
Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel

Q.44 The truth table of a monoshot shown in the figure is given in the table below:

X	Y	Q	$\overline{Q}$
0	↑	⊓	⊔
↓	1	⊓	⊔



Two monoshots, one positive edge triggered and other negative edge triggered, are connected as shown in the figure. The pulse widths of the two monoshot outputs, Q_1 and Q_2, are T_{ON_1} and T_{ON_2} respectively.



The frequency and the duty cycle of the signal at Q_1 will respectively be

(A) $f = \dfrac{1}{T_{ON_1} + T_{ON_2}}, D = \dfrac{T_{ON_1}}{T_{ON_1} + T_{ON_2}}$

(B) $f = \dfrac{1}{T_{ON_1} + T_{ON_2}}, D = \dfrac{T_{ON_2}}{T_{ON_1} + T_{ON_2}}$

(C) $f = \dfrac{1}{T_{ON_1}}, \quad D = \dfrac{T_{ON_1}}{T_{ON_1} + T_{ON_2}}$

(D) $f = \dfrac{1}{T_{ON_2}}, \quad D = \dfrac{T_{ON_1}}{T_{ON_1} + T_{ON_2}}$

Q.45 The contents (in Hexadecimal) of some of the memory locations in an 8085A based system are given below:

Address	Contents
..	..
26FE	00
26FF	01
2700	02
2701	03
2702	04
..	..

The contents of stack pointer (SP), program counter (PC) and (H,L) are 2700H, 2100H and 0000H respectively. When the following sequence of instructions are executed,

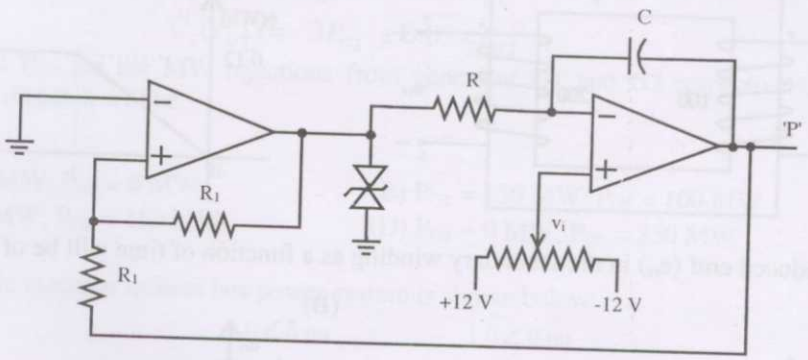
2100 H: DAD SP
2101 H: PCHL

the contents of (SP) and (PC) at the end of execution will be

(A) (PC) = 2102H, (SP) = 2700H
(B) (PC) = 2700H, (SP) = 2700H
(C) (PC) = 2800H, (SP) = 26FEH
(D) (PC) = 2A02H, (SP) = 2702H

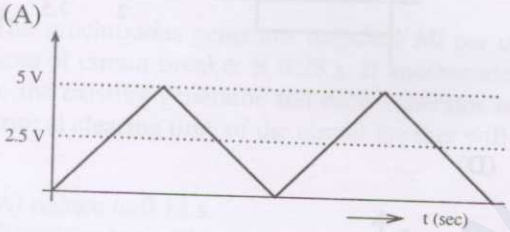
Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel
12/24

Q.46 A waveform generator circuit using OPAMPs is shown in the figure. It produces a triangular wave at point 'P' with a peak to peak voltage of 5 V for $v_i = 0$ V.

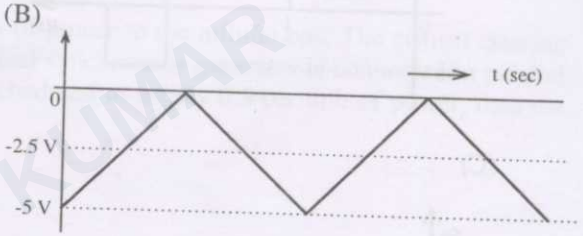


If the voltage v_i is made +2.5V, the voltage waveform at point 'P' will become

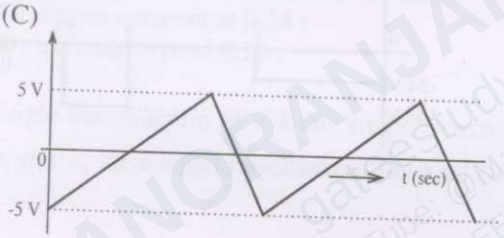
(A)



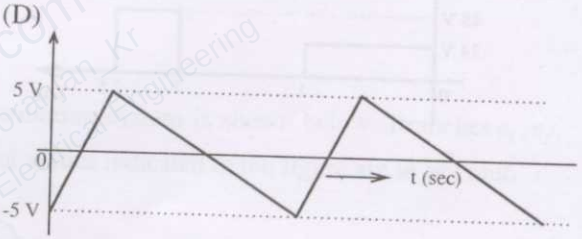
(B)



(C)



(D)



Q.47 A 230V, 50 Hz, 4-pole, single-phase induction motor is rotating in the clockwise (forward) direction at a speed of 1425 rpm. If the rotor resistance at standstill is 7.8 Ω, then the effective rotor resistance in the backward branch of the equivalent circuit will be

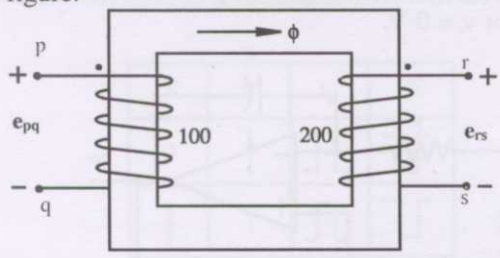
(A) 2 Ω (B) 4 Ω (C) 78 Ω (D) 156 Ω

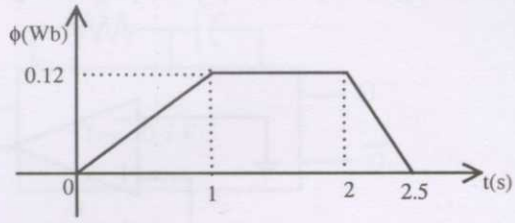
Q.48 A 400 V, 50 Hz, 30 hp, three-phase induction motor is drawing 50 A current at 0.8 power factor lagging. The stator and rotor copper losses are 1.5 kW and 900 W respectively. The friction and windage losses are 1050 W and the core losses are 1200 W. The air-gap power of the motor will be

(A) 23.06 kW (B) 24.11 kW (C) 25.01 kW (D) 26.21 kW

Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel

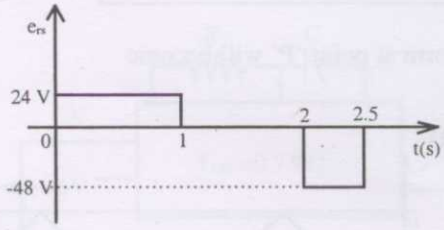
Q.49 The core of a two-winding transformer is subjected to a magnetic flux variation as indicated in the figure.



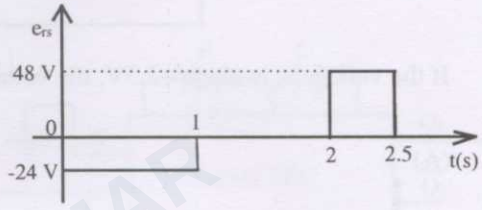


The induced emf (e_{rs}) in the secondary winding as a function of time will be of the form

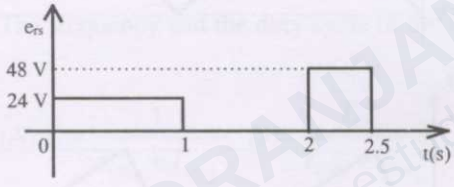
(A)



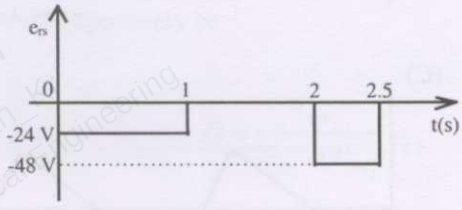
(B)



(C)



(D)



Q.50 Voltage phasors at the two terminals of a transmission line of length 70 km have a magnitude of 1.0 per unit but are 180 degrees out of phase. Assuming that the maximum load current in the line is $1/5^{th}$ of minimum 3-phase fault current, which one of the following transmission line protection schemes will NOT pick up for this condition?

(A) Distance protection using mho relays with zone-1 set to 80% of the line impedance
(B) Directional overcurrent protection set to pick up at 1.25 times the maximum load current
(C) Pilot relaying system with directional comparison scheme
(D) Pilot relaying system with segregated phase comparison scheme

Q.51 A lossless transmission line having Surge Impedance Loading (SIL) of 2280 MW is provided with a uniformly distributed series capacitive compensation of 30 %. Then, SIL of the compensated transmission line will be

(A) 1835 MW
(B) 2280 MW
(C) 2725 MW
(D) 3257 MW

Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel
14/24

Q.52 A lossless power system has to serve a load of 250 MW. There are two generators (G1 and G2) in the system with cost curves C_1 and C_2 respectively defined as follows:

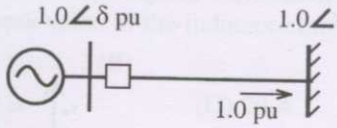
$$C_1(P_{G1}) = P_{G1} + 0.055 \times P_{G1}^2$$

$$C_2(P_{G2}) = 3P_{G2} + 0.03 \times P_{G2}^2$$

where P_{G1} and P_{G2} are the MW injections from generator G1 and G2 respectively. Then, the minimum cost dispatch will be

(A) P_{G1} = 250 MW; P_{G2} = 0 MW
(B) P_{G1} = 150 MW; P_{G2} = 100 MW
(C) P_{G1} = 100 MW; P_{G2} = 150 MW
(D) P_{G1} = 0 MW; P_{G2} = 250 MW

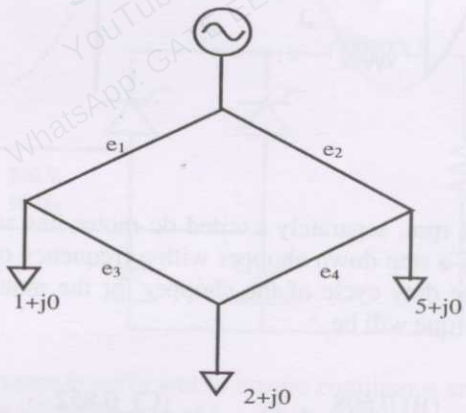
Q.53 A lossless single machine infinite bus power system is shown below:



The synchronous generator transfers 1.0 per unit of power to the infinite bus. The critical clearing time of circuit breaker is 0.28 s. If another identical synchronous generator is connected in parallel to the existing generator and each generator is scheduled to supply 0.5 per unit of power, then the critical clearing time of the circuit breaker will

(A) reduce to 0.14 s
(B) reduce but will be more than 0.14 s
(C) remain constant at 0.28 s
(D) increase beyond 0.28 s

Q.54 Single line diagram of a 4-bus single source distribution system is shown below. Branches e_1, e_2, e_3 and e_4 have equal impedances. The load current values indicated in the figure are in per unit.



Distribution company's policy requires radial system operation with minimum loss. This can be achieved by opening of the branch

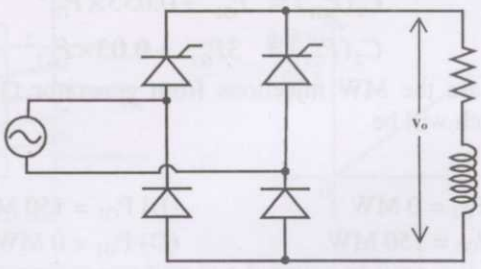
(A) e_1 (B) e_2 (C) e_3 (D) e_4

Q.55 A single phase fully controlled bridge converter supplies a load drawing constant and ripple free load current. If the triggering angle is 30^0, the input power factor will be

(A) 0.65 (B) 0.78 (C) 0.85 (D) 0.866

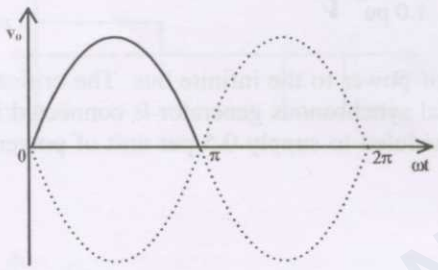
Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel
15/24

Q.56 A single-phase half controlled converter shown in the figure is feeding power to highly inductive load. The converter is operating at a firing angle of 60°.

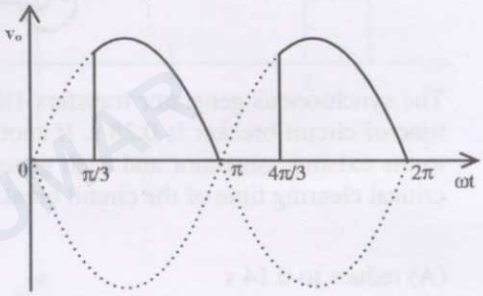


If the firing pulses are suddenly removed, the steady state voltage (v_o) waveform of the converter will become

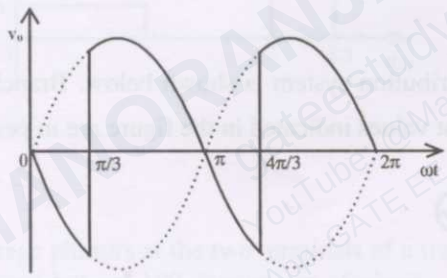
(A)



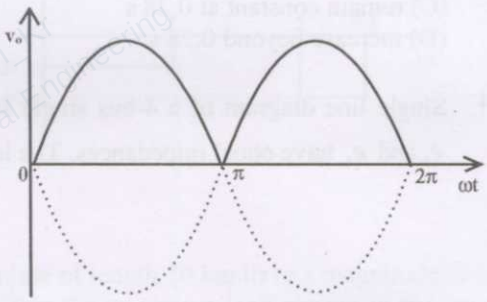
(B)



(C)



(D)



Q.57 A 220 V, 20 A, 1000 rpm, separately excited dc motor has an armature resistance of 2.5 Ω. The motor is controlled by a step down chopper with a frequency of 1 kHz. The input dc voltage to the chopper is 250 V. The duty cycle of the chopper for the motor to operate at a speed of 600 rpm delivering the rated torque will be

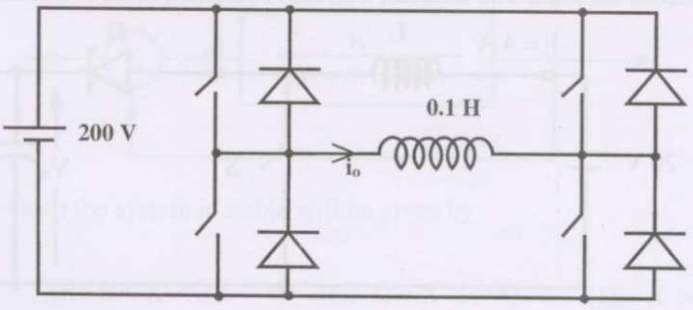
(A) 0.518 (B) 0.608 (C) 0.852 (D) 0.902

Q.58 A 220 V, 1400 rpm, 40 A separately excited dc motor has an armature resistance of 0.4 Ω. The motor is fed from a single phase circulating current dual converter with an input ac line voltage of 220 V (rms). The approximate firing angles of the dual converter for motoring operation at 50% of rated torque and 1000 rpm will be

(A) 43^0, 137^0 (B) 43^0, 47^0 (C) 39^0, 141^0 (D) 39^0, 51^0

Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel
16/24

Q.59 A single phase voltage source inverter is feeding a purely inductive load as shown in the figure.



The inverter is operated at 50 Hz in 180^0 square wave mode. Assume that the load current does not have any dc component. The peak value of the inductor current i_o will be

(A) 6.37 A (B) 10 A (C) 20 A (D) 40 A

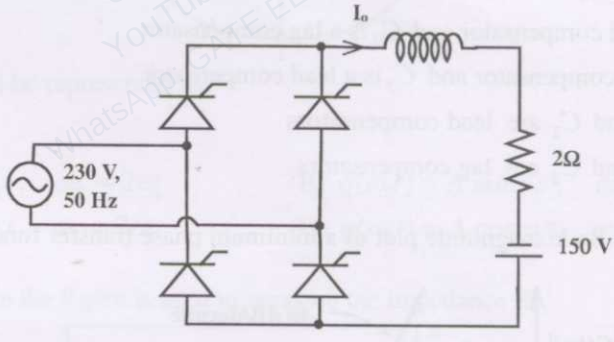
Q.60 A 400 V, 50 Hz, 4 pole, 1400 rpm, star connected squirrel cage induction motor has the following parameters referred to the stator:

$$R_r' = 1.0\ \Omega,\ X_s = X_r' = 1.5\ \Omega$$

Neglect stator resistance and core and rotational losses of the motor.
The motor is controlled from a 3-phase voltage source inverter with constant V/f control. The stator line-to-line voltage (rms) and frequency to obtain the maximum torque at starting will be:

(A) 20.6 V, 2.7 Hz (B) 133.3 V, 16.7 Hz (C) 266.6 V, 33.3 Hz (D) 323.3 V, 40.3 Hz

Q.61 A single phase fully controlled converter bridge is used for electrical braking of a separately excited dc motor. The dc motor load is represented by an equivalent circuit as shown in the figure.



Assume that the load inductance is sufficient to ensure continuous and ripple free load current. The firing angle of the bridge for a load current of $I_0 = 10$ A will be

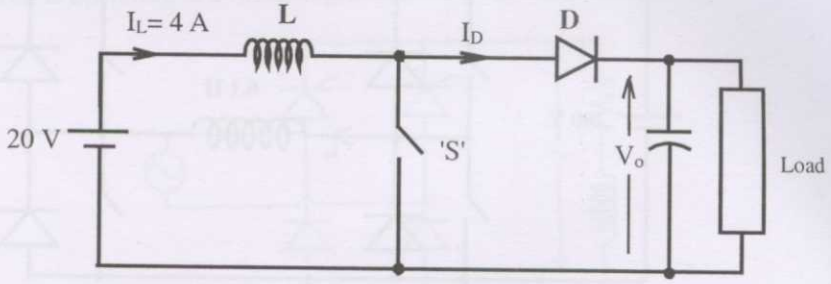
(A) 44° (B) 51° (C) 129° (D) 136°

Q.62 A three phase fully controlled bridge converter is feeding a load drawing a constant and ripple free load current of 10 A at a firing angle of 30°. The approximate Total Harmonic Distortion (%THD) and the rms value of fundamental component of the input current will respectively be

(A) 31% and 6.8 A (B) 31% and 7.8 A (C) 66% and 6.8 A (D) 66% and 7.8 A

Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel 17/24

Q.63 In the circuit shown in the figure, the switch is operated at a duty cycle of 0.5. A large capacitor is connected across the load. The inductor current is assumed to be continuous.



The average voltage across the load and the average current through the diode will respectively be

(A) 10 V, 2 A (B) 10 V, 8 A (C) 40 V, 2 A (D) 40 V, 8 A

Q.64 The transfer function of a linear time invariant system is given as

$$G(s) = \frac{1}{s^2 + 3s + 2}$$

The steady state value of the output of this system for a unit impulse input applied at time instant $t = 1$ will be

(A) 0 (B) 0.5 (C) 1 (D) 2

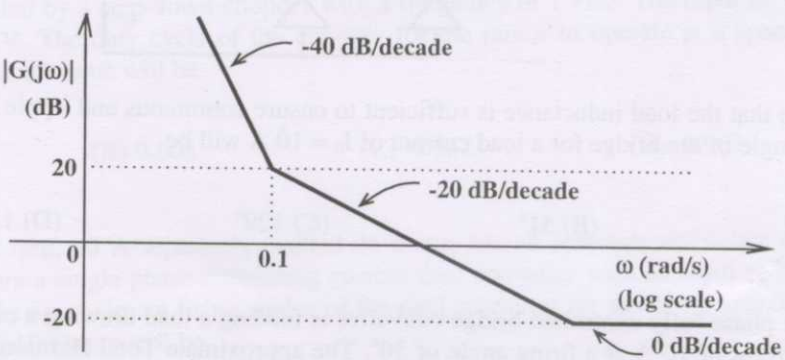
Q.65 The transfer functions of two compensators are given below:

$$C_1 = \frac{10\,(s+1)}{(s+10)}, \quad C_2 = \frac{s+10}{10\,(s+1)}$$

Which one of the following statements is correct?

(A) C_1 is a lead compensator and C_2 is a lag compensator
(B) C_1 is a lag compensator and C_2 is a lead compensator
(C) Both C_1 and C_2 are lead compensators
(D) Both C_1 and C_2 are lag compensators

Q.66 The asymptotic Bode magnitude plot of a minimum phase transfer function is shown in the figure:

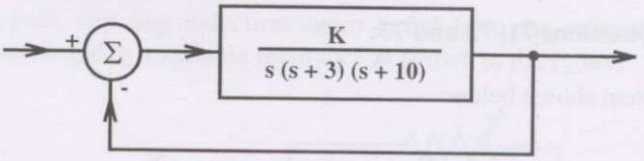


This transfer function has

(A) Three poles and one zero (B) Two poles and one zero
(C) Two poles and two zeros (D) One pole and two zeros

Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel 18/24

Q.67 Figure shows a feedback system where K > 0.



The range of K for which the system is stable will be given by

(A) $0 < K < 30$ (B) $0 < K < 39$ (C) $0 < K < 390$ (D) $K > 390$

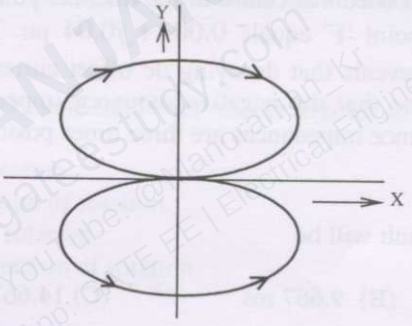
Q.68 The transfer function of a system is given as

$$\frac{100}{s^2 + 20s + 100}$$

This system is

(A) an overdamped system
(B) an underdamped system
(C) a critically damped system
(D) an unstable system

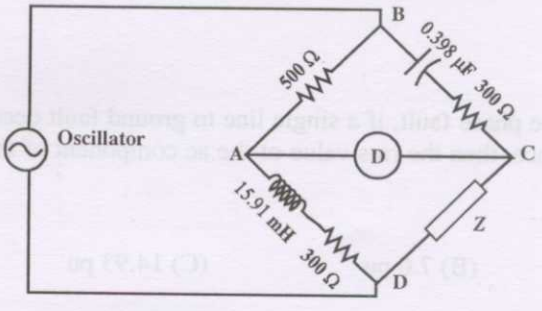
Q.69 Two sinusoidal signals $p(\omega_1 t) = A \sin \omega_1 t$ and $q(\omega_2 t)$ are applied to X and Y inputs of a dual channel CRO. The Lissajous figure displayed on the screen is shown below:



The signal $q(\omega_2 t)$ will be represented as

(A) $q(\omega_2 t) = A \sin \omega_2 t, \quad \omega_2 = 2\omega_1$
(B) $q(\omega_2 t) = A \sin \omega_2 t, \quad \omega_2 = \omega_1 / 2$
(C) $q(\omega_2 t) = A \cos \omega_2 t, \quad \omega_2 = 2\omega_1$
(D) $q(\omega_2 t) = A \cos \omega_2 t, \quad \omega_2 = \omega_1 / 2$

Q.70 The ac bridge shown in the figure is used to measure the impedance Z.



If the bridge is balanced for oscillator frequency f =2 kHz, then the impedance Z will be

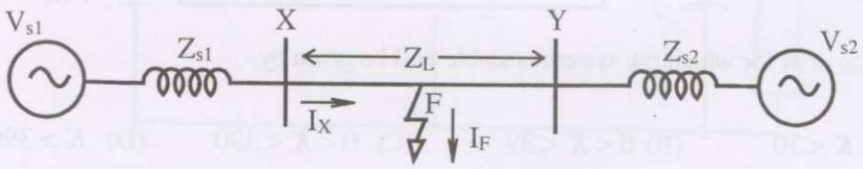
(A) (260 + j0) Ω (B) (0 + j200) Ω (C) (260 – j200) Ω (D) (260 + j200) Ω

Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel 19/24

Common Data Questions

Common Data for Questions 71, 72 and 73:

Consider a power system shown below:



Given that:

$V_{s1} = V_{s2} = 1.0 + j0.0$ pu;

The positive sequence impedances are $Z_{s1} = Z_{s2} = 0.001 + j0.01$ pu and $Z_L = 0.006 + j0.06$ pu.

3-phase Base MVA = 100

Voltage base = 400 kV (Line to Line)

Nominal system frequency = 50 Hz

The reference voltage for phase 'a' is defined as $v(t) = V_m \cos(\omega t)$.

A symmetrical three phase fault occurs at centre of the line, i.e. point 'F' at time t_o. The positive sequence impedance from source S_1 to point 'F' equals $0.004 + j0.04$ pu. The waveform corresponding to phase 'a' fault current from bus X reveals that decaying dc offset current is negative and in magnitude at its maximum initial value. Assume that the negative sequence impedances are equal to positive sequence impedances, and the zero sequence impedances are three times positive sequence impedances.

Q.71 The instant (t_o) of the fault will be

(A) 4.682 ms (B) 9.667 ms (C) 14.667 ms (D) 19.667 ms

Q.72 The rms value of the ac component of fault current (I_X) will be

(A) 3.59 kA (B) 5.07 kA (C) 7.18 kA (D) 10.15 kA

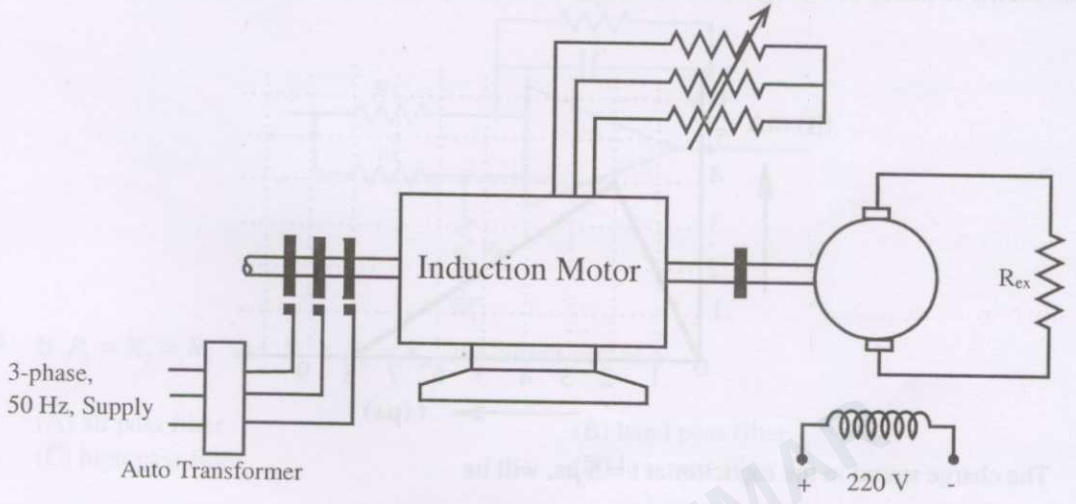
Q.73 Instead of the three phase fault, if a single line to ground fault occurs on phase 'a' at point 'F' with zero fault impedance, then the rms value of the ac component of fault current (I_X) for phase 'a' will be

(A) 4.97 pu (B) 7.0 pu (C) 14.93 pu (D) 29.85 pu

Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel
20/24

Common Data for Questions 74 and 75:

A 3-phase, 440 V, 50 Hz, 4-pole, slip ring induction motor is fed from the rotor side through an auto-transformer and the stator is connected to a variable resistance as shown in the figure.



The motor is coupled to a 220 V, separately excited, dc generator feeding power to fixed resistance of 10 Ω. Two-wattmeter method is used to measure the input power to induction motor. The variable resistance is adjusted such that the motor runs at 1410 rpm and the following readings were recorded:

$W_1 = 1800$ W, $W_2 = -200$ W

Q.74 The speed of rotation of stator magnetic field with respect to rotor structure will be

(A) 90 rpm in the direction of rotation.
(B) 90 rpm in the opposite direction of rotation.
(C) 1500 rpm in the direction of rotation
(D) 1500 rpm in the opposite direction of rotation

Q.75 Neglecting all losses of both the machines, the dc generator power output and the current through resistance (R_{ex}) will respectively be

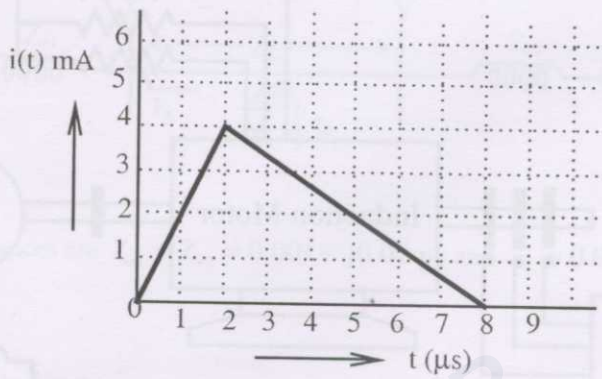
(A) 96 W, 3.10 A
(B) 120 W, 3.46 A
(C) 1504 W, 12.26 A
(D) 1880 W, 13.71 A

Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel 21/24

Linked Answer Questions: Q.76 to Q.85 carry two marks each.

Statement for Linked Answer Questions 76 and 77:

The current *i(t)* sketched in the figure flows through an initially uncharged 0.3 nF capacitor.



Q.76 The charge stored in the capacitor at t = 5 μs, will be

(A) 8 nC (B) 10 nC (C) 13 nC (D) 16 nC

Q.77 The capacitor charged upto 5 μs, as per the current profile given in the figure, is connected across an inductor of 0.6 mH. Then the value of voltage across the capacitor after 1 μs will approximately be

(A) 18.8 V (B) 23.5 V (C) -23.5 V (D) -30.6 V

Statement for Linked Answer Questions 78 and 79:

The state space equation of a system is described by

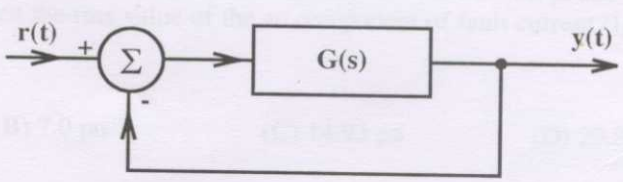
$$\dot{\mathbf{x}} = \mathbf{Ax} + \mathbf{Bu}$$
$$\mathbf{y} = \mathbf{Cx}$$

where x is state vector, u is input, y is output and $\mathbf{A} = \begin{bmatrix} 0 & 1 \\ 0 & -2 \end{bmatrix}, \mathbf{B} = \begin{bmatrix} 0 \\ 1 \end{bmatrix}, \mathbf{C} = \begin{bmatrix} 1 & 0 \end{bmatrix}$.

Q.78 The transfer function $G(s)$ of this system will be

(A) $\frac{s}{(s+2)}$ (B) $\frac{s+1}{s(s-2)}$ (C) $\frac{s}{(s-2)}$ (D) $\frac{1}{s(s+2)}$

Q.79 A unity feedback is provided to the above system $G(s)$ to make it a closed loop system as shown in figure.



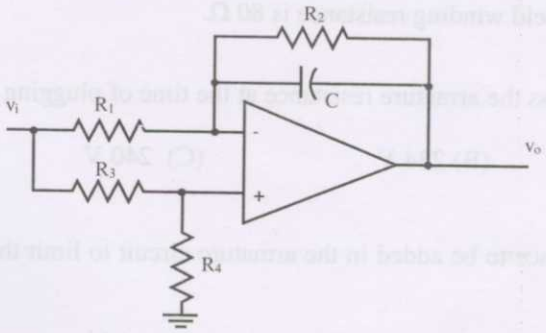
For a unit step input $r(t)$, the steady state error in the output will be

(A) 0 (B) 1 (C) 2 (D) ∞

Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel
22/24

Statement for Linked Answer Questions 80 and 81:

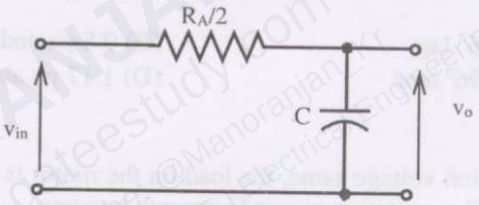
A general filter circuit is shown in the figure:



Q.80 If $R_1 = R_2 = R_A$ and $R_3 = R_4 = R_B$, the circuit acts as a

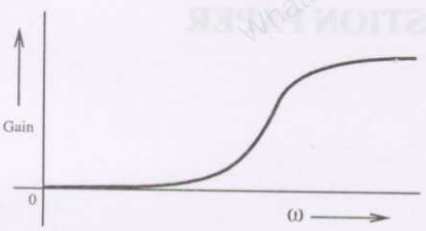
(A) all pass filter
(B) band pass filter
(C) high pass filter
(D) low pass filter

Q.81 The output of the filter in Q.80 is given to the circuit shown in figure:

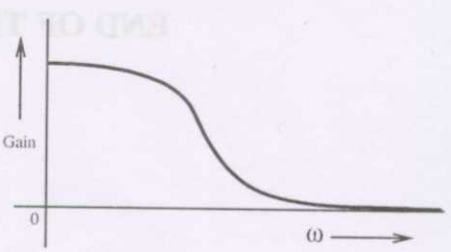


The gain vs frequency characteristic of the output (v_o) will be

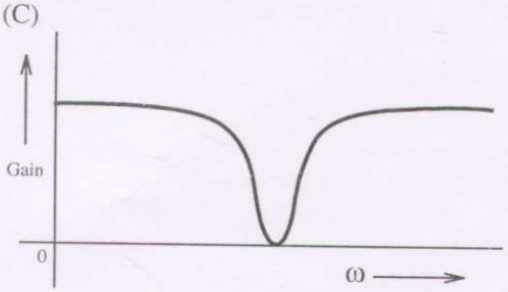
(A)



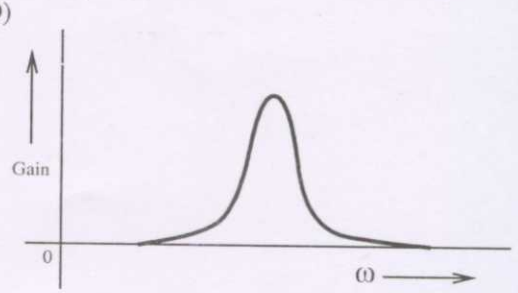
(B)



(C)



(D)



Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel

Statement for Linked Answer Questions 82 and 83:

A 240 V, dc shunt motor draws 15 A while supplying the rated load at a speed of 80 rad/s. The armature resistance is 0.5 Ω and the field winding resistance is 80 Ω.

Q.82 The net voltage across the armature resistance at the time of plugging will be

(A) 6 V (B) 234 V (C) 240 V (D) 474 V

Q.83 The external resistance to be added in the armature circuit to limit the armature current to 125% of its rated value is

(A) 31.1 Ω (B) 31.9 Ω (C) 15.1 Ω (D) 15.9 Ω

Statement for Linked Answer Questions 84 and 85:

A synchronous motor is connected to an infinite bus at 1.0 pu voltage and draws 0.6 pu current at unity power factor. Its synchronous reactance is 1.0 pu and resistance is negligible.

Q.84 The excitation voltage (E) and load angle (δ) will respectively be

(A) 0.8 pu and 36.86° lag
(B) 0.8 pu and 36.86° lead
(C) 1.17 pu and 30.96° lead
(D) 1.17 pu and 30.96° lag

Q.85 Keeping the excitation voltage same, the load on the motor is increased such that the motor current increases by 20%. The operating power factor will become

(A) 0.995 lagging (B) 0.995 leading (C) 0.791 lagging (D) 0.848 leading

END OF THE QUESTION PAPER

Manoranjan Kumar gateestudy.com
YouTube: @Manoranjan_Kr
WhatsApp: GATE EE | Electrical Engineering - Follow channel
24/24